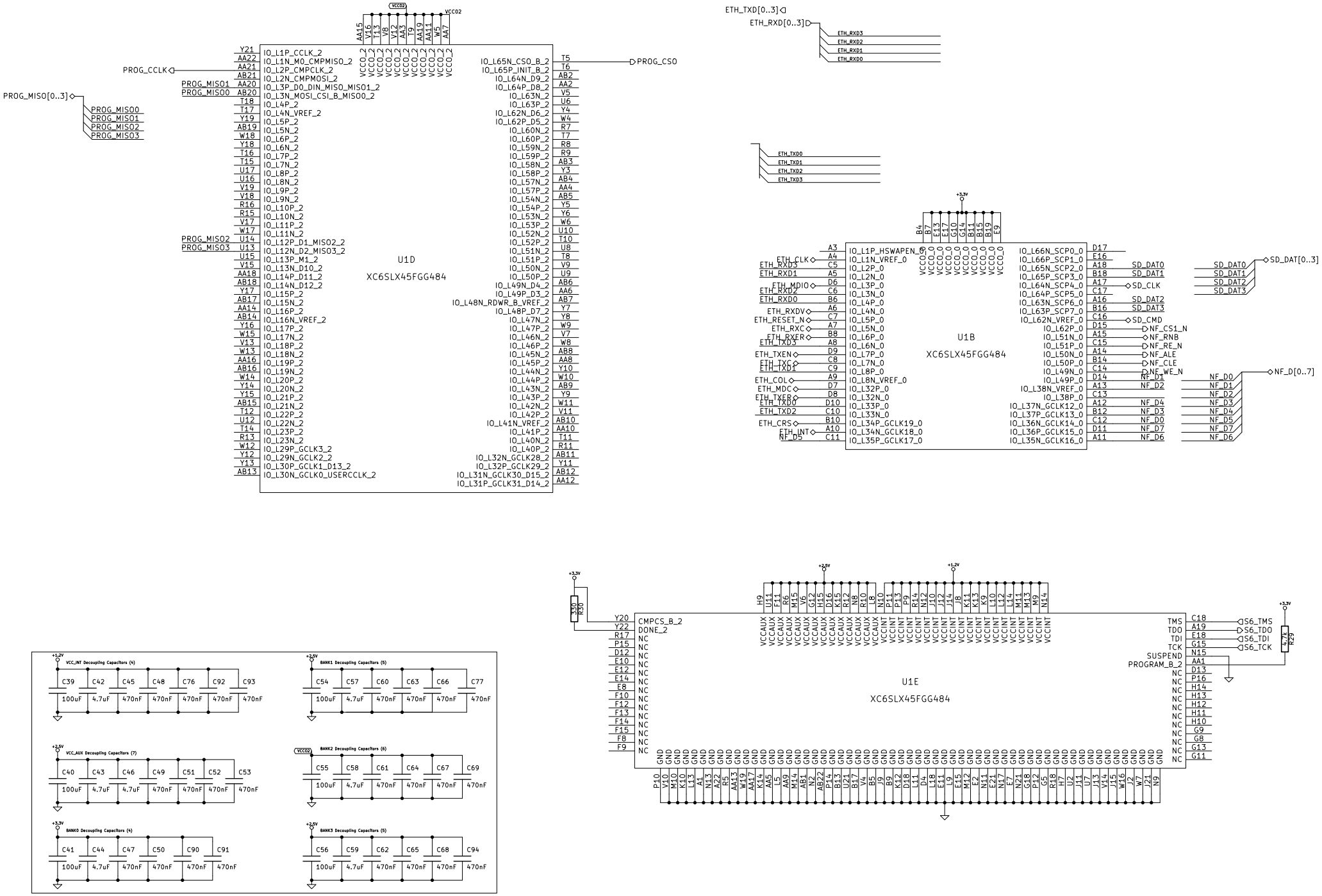
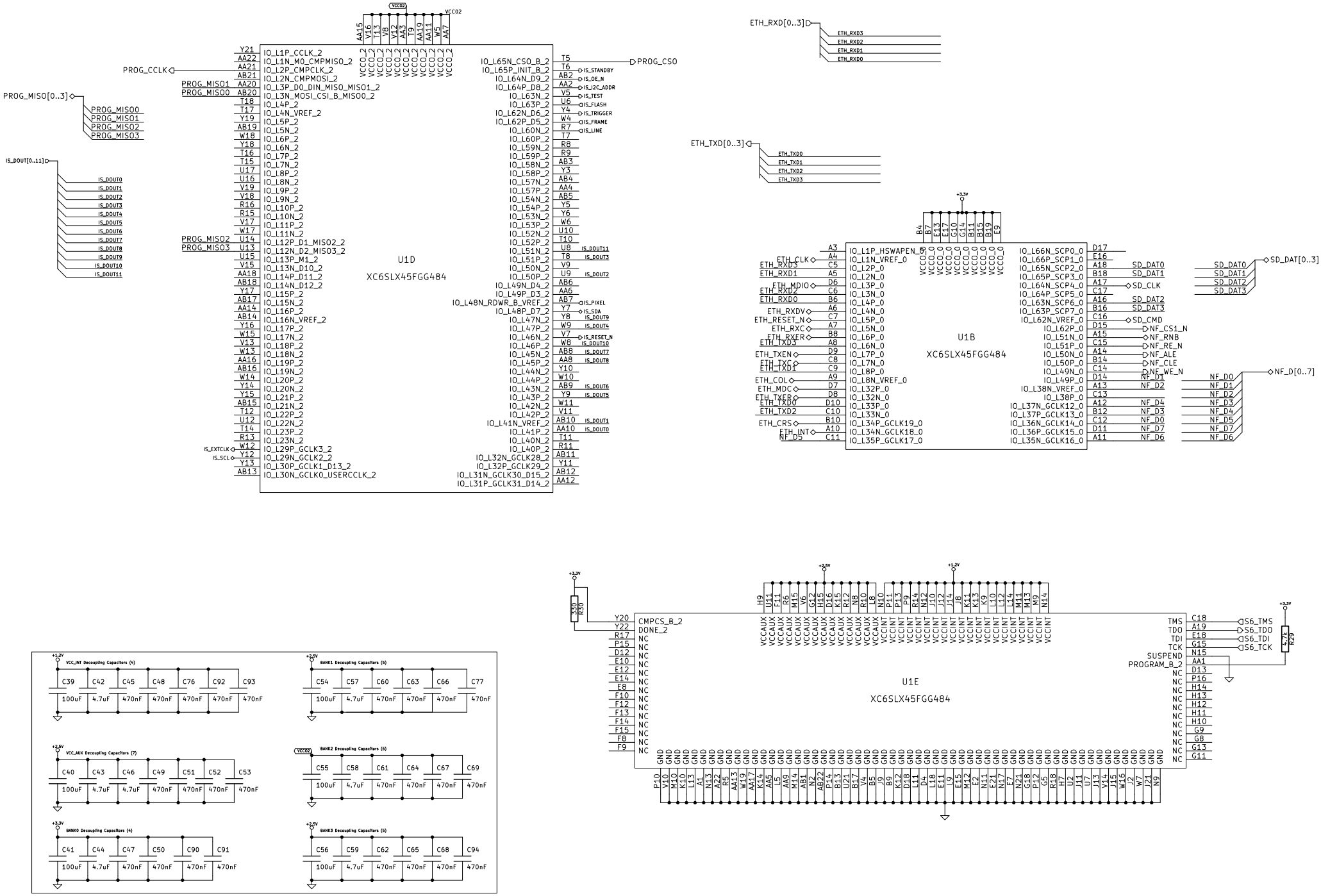

U1D
XC6SLX45FGG484
U1B
XC6SLX45FGG484
U1E
XC6SLX45FGG484
PROG_CCLK
PROG_MISO[0..3]
PROG_MISO0
PROG_MISO1
PROG_MISO2
PROG_MISO3
PROG_CS0
ETH_TXD[0..3]
ETH_RXD[0..3]
ETH_RXD3
ETH_RXD2
ETH_RXD1
ETH_RXD0
ETH_TXD0
ETH_TXD1
ETH_TXD2
ETH_TXD3
SD_DAT[0..3]
SD_DAT0
SD_DAT1
SD_DAT2
SD_DAT3
SD_CLK
SD_CMD
NF_D[0..7]
NF_D0
NF_D1
NF_D2
NF_D3
NF_D4
NF_D5
NF_D6
NF_D7
NF_CS1_N
NF_RNB
NF_RE_N
NF_ALE
NF_CLE
NF_WE_N
ETH_CLK
ETH_MDIO
ETH_RXDV
ETH_RESET_N
ETH_RXC
ETH_RXER
ETH_TXEN
ETH_TXC
ETH_COL
ETH_MDC
ETH_CRS
ETH_INT
S6_TMS
S6_TDO
S6_TDI
S6_TCK
VCC_INT Decoupling Capacitors (4)
BANK1 Decoupling Capacitors (5)
VCC_AUX Decoupling Capacitors (7)
BANK2 Decoupling Capacitors (6)
BANK0 Decoupling Capacitors (4)
BANK3 Decoupling Capacitors (5)


U1D
XC6SLX45FGG484
U1B
XC6SLX45FGG484
U1E
XC6SLX45FGG484
PROG_CCLK
PROG_MISO[0..3]
PROG_MISO0
PROG_MISO1
PROG_MISO2
PROG_MISO3
IS_DOUT[0..11]
IS_DOUT0
IS_DOUT1
IS_DOUT2
IS_DOUT3
IS_DOUT4
IS_DOUT5
IS_DOUT6
IS_DOUT7
IS_DOUT8
IS_DOUT9
IS_DOUT10
IS_DOUT11
IS_EXTCLK
IS_SCL
PROG_CS0
IS_STANDBY
IS_OE_N
IS_I2C_ADDR
IS_TEST
IS_FLASH
IS_TRIGGER
IS_FRAME
IS_LINE
IS_PIXEL
IS_SDA
IS_RESET_N
ETH_RXD[0..3]
ETH_RXD3
ETH_RXD2
ETH_RXD1
ETH_RXD0
ETH_TXD[0..3]
ETH_TXD0
ETH_TXD1
ETH_TXD2
ETH_TXD3
ETH_CLK
ETH_MDIO
ETH_RXDV
ETH_RESET_N
ETH_RXC
ETH_RXER
ETH_TXD3
ETH_TXEN
ETH_TXC
ETH_TXD1
ETH_COL
ETH_MDC
ETH_TXER
ETH_TXD0
ETH_CRS
ETH_INT
NF_D5
SD_DAT0
SD_DAT1
SD_DAT2
SD_DAT3
SD_CLK
SD_CMD
SD_DAT[0..3]
NF_CS1_N
NF_RNB
NF_RE_N
NF_ALE
NF_CLE
NF_WE_N
NF_D1
NF_D2
NF_D4
NF_D3
NF_D0
NF_D7
NF_D6
NF_D[0..7]
S6_TMS
S6_TDO
S6_TDI
S6_TCK
TMS
TDO
TDI
TCK
SUSPEND
PROGRAM_B_2
CMPCS_B_2
DONE_2
330
R30
4.7K
R29
VCC_INT Decoupling Capacitors (4)
BANK1 Decoupling Capacitors (5)
VCC_AUX Decoupling Capacitors (7)
BANK2 Decoupling Capacitors (6)
BANK0 Decoupling Capacitors (4)
BANK3 Decoupling Capacitors (5)